

Key Specifications of the ARM1176 Prototyping Kit

Evaluation Board	LSI	ARM processors evaluation LSI and FPGA
	External Bus	64 bit External AXI (*1) (DDR, max. 100 MHz)
	Memory	Flash memory 32MB Mobile DDR SDRAM 128 MB
	External Interface	Ethernet, UART, ICE
LSI used to evaluate ARM processor	Multi-core processor	ARM1176JZF-S (max. 500 MHz), ARM926EJ-S (max. 400 MHz), ARM946E-S (max. 400 MHz)
	Process technology	CMOS 90nm low power-consumption process
	Package	FCBGA (*2) 1156 (35mm x 35mm)
	Power consumption	1.2 W (ARM1176JZF-S: operating at 500MHz, typ.)
Other	FPGA reference design, sample program, simulation model, user's manual	

****1:** AXI:
Advanced eXtensible Interface. A high-speed bus conforming to ARM's AMBA 3.0 specification. Used as processor bus for ARM1176JZF-S

****2:** FCBGA:
A multi-pin package that includes flip-chip bonding technologies.